



COLLEGE : GOA COLLEGE OF ENGINEERING

235  
9/5/16

Seat No : 3101	P R No : 201507401	Sex : M	Name : BHASALE VINAY BHANUDAS
No Of Attempts : 1			
	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BB P	
IA	2	BB P	
Digital VLSI Design			
Theory	4	BC P	
IA	2	BB P	
Analog VLSI Design			
Theory	4	CC P	
IA	2	AB P	
VLSI Technology			
Theory	4	BB P	
IA	2	BB P	
Advanced Engineering Mathematics			
Theory	4	BC P	
IA	2	CC P	
HDL - LAB			
IA	2	AB P	
Practical	2	AA P	
VLSI CAD - LAB			
IA	2	BB P	
Practical	2	BB P	
<b>Total :</b>	<b>38</b>		<b>6.68 P</b>
			<b>PASSES</b>

Seat No : 3102	P R No : 201107681	Sex : M	Name : BORKAR NEEL RAMAKANT
No Of Attempts : 1			
	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BB P	
IA	2	AB P	
Digital VLSI Design			
Theory	4	CC P	
IA	2	AB P	
Analog VLSI Design			
Theory	4	AB P	
IA	2	AB P	
VLSI Technology			
Theory	4	AB P	
IA	2	AB P	
Advanced Engineering Mathematics			
Theory	4	BC P	
IA	2	BC P	
HDL - LAB			
IA	2	AB P	
Practical	2	BB P	
VLSI CAD - LAB			
IA	2	BB P	
Practical	2	BC P	
<b>Total :</b>	<b>38</b>		<b>7.05 P</b>
			<b>PASSES</b>

101  
10/5/16

NB/WB/HEICD/

A

KAP



COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3103 P R No : 201108381 Sex : F Name : GOVEKAR DIVYA DIGAMBAR  
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	CC P	
	IA	2	BB P	
Digital VLSI Design	Theory	4	FF F	
	IA	2	CC P	
Analog VLSI Design	Theory	4	CC P	
	IA	2	BB P	
VLSI Technology	Theory	4	CC P	
	IA	2	BC P	
Advanced Engineering Mathematics	Theory	4	CC P	
	IA	2	BC P	
HDL - LAB	IA	2	AA P	
	Practical	2	AB P	
VLSI CAD - LAB	IA	2	BB P	
	Practical	2	BC P	
Total :		38		5.32 F FAILS

Seat No : 3105 P R No : 200907827 Sex : F Name : KHANDEPARKAR SHWETA MAHENDRA  
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	AB P	
	IA	2	AB P	
Digital VLSI Design	Theory	4	AB P	
	IA	2	AB P	
Analog VLSI Design	Theory	4	BB P	
	IA	2	AB P	
VLSI Technology	Theory	4	AB P	
	IA	2	AB P	
Advanced Engineering Mathematics	Theory	4	BB P	
	IA	2	AB P	
HDL - LAB	IA	2	AA P	
	Practical	2	AA P	
VLSI CAD - LAB	IA	2	AB P	
	Practical	2	BB P	
Total :		38		7.84 P PASSES

KAP



REVALUATION RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I  
EXAMINATION HELD IN NOVEMBER 2015  
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3106 P R No : 201008499 Sex : F Name : KHARANGATE LAXIMI SHASHANK  
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	AB P	
IA	2	AA P	
Digital VLSI Design			
Theory	4	BB P	
IA	2	AB P	
Analog VLSI Design			
Theory	4	AB P	
IA	2	AB P	
VLSI Technology			
Theory	4	AB P	
IA	2	AA P	
Advanced Engineering Mathematics			
Theory	4	BC P	
IA	2	BB P	
HDL - LAB			
IA	2	AA P	
Practical	2	AB P	
VLSI CAD - LAB			
IA	2	AB P	
Practical	2	AB P	
Total :	38		7.79 P PASSES

Seat No : 3108 P R No : 201107707 Sex : M Name : MAHATME MOHIT BHAIRAV  
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BC P	
IA	2	BB P	
Digital VLSI Design			
Theory	4	CC P	
IA	2	CC P	
Analog VLSI Design			
Theory	4	BC P	
IA	2	BC P	
VLSI Technology			
Theory	4	CC P	
IA	2	BB P	
Advanced Engineering Mathematics			
Theory	4	CC P	
IA	2	BC P	
HDL - LAB			
IA	2	AA P	
Practical	2	AB P	
VLSI CAD - LAB			
IA	2	AB P	
Practical	2	BB P	
Total :	38		6.16 P PASSES



REVALUATION RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I  
EXAMINATION HELD IN NOVEMBER 2015  
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3109 P R No : 201007353 Sex : F Name : MAYEKAR MITHILI NARAYAN  
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	BC P	
	IA	2	BC P	
Digital VLSI Design	Theory	4	CC P	
	IA	2	CC P	
Analog VLSI Design	Theory	4	CC P	
	IA	2	BC P	
VLSI Technology	Theory	4	CC P	
	IA	2	CC P	
Advanced Engineering Mathematics	Theory	4	FF F	
	IA	2	CC P	
HDL - LAB	IA	2	AB P	
	Practical	2	BB P	
VLSI CAD - LAB	IA	2	BC P	
	Practical	2	CC P	
Total :		38		5.00 F FAILS

Seat No : 3110 P R No : 201006308 Sex : F Name : NAIK STHRIGDHARA NAVANATH  
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	BC P	
	IA	2	AB P	
Digital VLSI Design	Theory	4	AB P	
	IA	2	BC P	
Analog VLSI Design	Theory	4	CC P	
	IA	2	BB P	
VLSI Technology	Theory	4	BC P	
	IA	2	BB P	
Advanced Engineering Mathematics	Theory	4	BC P	
	IA	2	CC P	
HDL - LAB	IA	2	BB P	
	Practical	2	BB P	
VLSI CAD - LAB	IA	2	BB P	
	Practical	2	BC P	
Total :		38		6.42 P PASSES

KAR



COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No :	P R No :	Sex :	Name :		
3112	201107719	M	PATIL ABHIJEET VILAS		
No Of Attempts :			No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics					
			Theory	4	FF F
			IA	2	BC P
Digital VLSI Design					
			Theory	4	FF F
			IA	2	CC P
Analog VLSI Design					
			Theory	4	FF F
			IA	2	BC P
VLSI Technology					
			Theory	4	CC P
			IA	2	CC P
Advanced Engineering Mathematics					
			Theory	4	CC P
			IA	2	CC P
HDL - LAB					
			IA	2	AB P
			Practical	2	BB P
VLSI CAD - LAB					
			IA	2	BB P
			Practical	2	CC P
			Total :	38	3.89 F FAILS

Seat No :	P R No :	Sex :	Name :		
3115	201007394	M	SHETGAONKAR ANAY ANIL		
No Of Attempts :			No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics					
			Theory	4	FF F
			IA	2	BC P
Digital VLSI Design					
			Theory	4	FF F
			IA	2	CC P
Analog VLSI Design					
			Theory	4	CC P
			IA	2	CC P
VLSI Technology					
			Theory	4	FF F
			IA	2	CC P
Advanced Engineering Mathematics					
			Theory	4	FF F
			IA	2	CC P
HDL - LAB					
			IA	2	BB P
			Practical	2	BB P
VLSI CAD - LAB					
			IA	2	BB P
			Practical	2	BC P
			Total :	38	3.32 F FAILS



COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3117 P R No : 201004714 Sex : F Name : KANADE SHRADDHA SABAJI  
No Of Attempts : 2

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	CC +	
IA	2	AA +	
Digital VLSI Design			
Theory	4	BC +	
IA	2	BB +	
Analog VLSI Design			
Theory	4	BC +	
IA	2	BB +	
VLSI Technology			
Theory	4	CC +	
IA	2	AB +	
Advanced Engineering Mathematics			
Theory	4	FF F	
IA	2	CC +	
HDL - LAB			
IA	2	AA +	
Practical	2	AA +	
VLSI CAD - LAB			
IA	2	AB +	
Practical	2	AB +	
Total :	38		6.00 F FAILS
NO CHANGE			

Seat No : 3120 P R No : 200806910 Sex : M Name : SATARDEKAR GIRISH GURUDAS  
No Of Attempts : 2

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BC +	
IA	2	BC +	
Digital VLSI Design			
Theory	4	BB +	
IA	2	BB +	
Analog VLSI Design			
Theory	4	BC +	
IA	2	BB +	
VLSI Technology			
Theory	4	BC P	
IA	2	AB +	
Advanced Engineering Mathematics			
Theory	4	CC P	
IA	2	CC +	
HDL - LAB			
IA	2	BC +	
Practical	2	BC +	
VLSI CAD - LAB			
IA	2	AB +	
Practical	2	BB +	
Total :	38		6.32 P PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : *[Signature]*  
Checked By : *KM*  
Date : 5/5/2016

*[Signature]* Assistant Registrar-E(Prof.)  
*[Signature]* Controller Of Examinations  
05/15/2016

*[Signature]* Prof. Y. V. Reddy  
Registrar



P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cumulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college  
Page 6 of 6